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PATENT
JPW

Case Docket No. IMEC211.001AUS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Vandersteen et al.
 Appl. No. : 09/591,026
 Filed : June 9, 2000
 For : METHOD FOR
 DETERMINING SIGNALS IN
 MIXED SIGNAL SYSTEMS
 Examiner : F. O. Ferris, III
 Group Art Unit : 2128

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10/16/04
(Date)

John M. Carson, Reg. No. 34,303

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

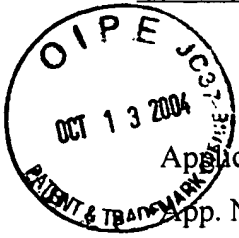
Dear Sir:

Enclosed for filing in the above-identified application are:

- (X) An Information Disclosure Statement.
- (X) A PTO Form 1449 with five (5) references.
- (X) The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Account No. 11-1410.
- (X) Return prepaid postcard.

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INFORMATION DISCLOSURE STATEMENT



Applicant	:	Vandersteen et al.
App. No.	:	09/591,026
Filed	:	June 9, 2000
For	:	METHOD FOR DETERMINING SIGNALS IN MIXED SIGNAL SYSTEMS
Examiner	:	F. O. Ferris, III
Group Art Unit	:	2128

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing five references that are also enclosed. Also enclosed is a copy of European Search Report.

This Information Disclosure Statement is being filed before the mailing date of a final action under 37 C.F.R. § 1.113 and before the mailing date of a Notice of Allowance under § 1.311. A certification under 37 C.F.R. § 1.97(e) is set forth below. Thus, no fee is required as set forth in 37 C.F.R. § 1.97(c).

CERTIFICATION UNDER 37 C.F.R. § 1.97(e)(1)

I hereby certify that each item of information contained in this Statement was first cited in a communication from a foreign Patent Office in a counterpart foreign application not more than thirty days prior to the filing of this Information Disclosure Statement.

Respectfully submitted,
KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: 10/6/04

By: [Signature]
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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)	ATTY. DOCKET NO. IMEC211.001AUS	APPLICATION NO. 09/591,026
	APPLICANT Vandersteen et al.	
	FILING DATE June 9, 2000	GROUP 2128

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

EXAMINER INITIAL	
1	Guindi et al., "High-Level Analog Synthesis Using Signal Flow Graph Transformations", (1995) IEEE pgs 366-369.
2	Grimm et al., "Repartitioning and Technology Mapping of Electronic Hybrid Systems", (1998) IEEE pgs 52-58.
3	Huijs, "A Graph Rewriting Approach for Transformational Design of Digital Systems", (1996) IEEE pgs 177-181.
4	Cottrell, "Event-Driven Behavioral Simulation of Analogue Transfer Functions", (1990) IEEE pgs 240-243.
5	Subramaniam et al., "Behavioral Modeling Techniques for Analog and Mixed Signal Design", (1994) IEEE pgs 319-322.

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EXAMINER	DATE CONSIDERED
*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.	